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AMENDMENT UNDER 37 CFR 1.116 EXPEDITED PROCEDURE – EXAMINING GROUP 2123

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J. Matthew Zigmant

PATENT
Attorney Docket No.: 020408-000300US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Vikram Gupta

Application No.: 09/618,971

Filed: July 19, 2000

For: METHOD FOR DESIGNING MIXED SIGNAL INTEGRATED CIRCUITS AND CONFIGURABLE SYNCHRONOUS DIGITAL NOISE EMULATOR CIRCUIT

Customer No.: 20350

Confirmation No. 1379

Examiner:

Kandasamy Thangavelu

Technology Center/Art Unit: 2123

AMENDMENT UNDER 37 CFR 1.116 EXPEDITED PROCEDURE EXAMINING GROUP 2123

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Commissioner for Patents

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Sir:

In response to the final office action mailed August 2, 2004 on the abovereferenced application, please enter the following amendments and remarks:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.